CYT7350 LDO Linear Voltage Regulator

CYT
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General Description

CYT7350 is a positive voltage regulator circuit with low dropout, high precision output voltage and low power consumption current developed by practical CMOS technology. Due to the built-in low-pass state resistance transistor, the dropout is low. With a high input voltage capacity, it can be suitable for high voltage application circuit when the highest working voltage up to 12V.

Electric Characteristics

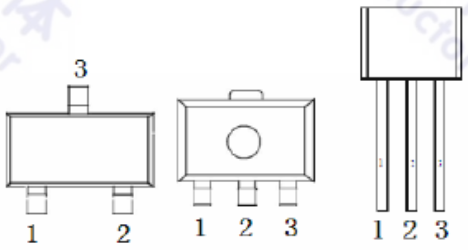
Unless otherwise stated, T_A=25°C.

Description	Symbol	Conditions	Min.	Typ.	Max.	Unit
Output voltage	V_{OUT}	V_{IN}=6V，I_{OUT}=40mA	4.85	5	5.15	V
Input voltage	V_{IN}	-	-	-	12	V
Output current	I_{OUT}	V_{IN}=6V，V_{OUT}≥4.5V	250	-	-	mA
Load regulation	$\triangle V_{OUT}$	V_{IN}=6V， 1mA≤I_{OUT}≤60mA	-	45	90	mV
Voltage sag	V_{DIF}	I_{OUT}=40mAl	-	60	-	mV
Quiescent current	I_{SS}	V_{IN}=6V，no-load	-	2	3	μA
Line regulation	$\triangle V_{OUT}/(\triangle V_{IN}*V_{OUT})$	6V≤V_{IN}≤12V， I_{OUT}=40mA	-	0.2	0.3	%/V
Temperature coefficient	$\triangle V_{OUT}/\triangle T_A$	V_{IN}=6V，I_{OUT}=40mA， 0℃≤T_A≤85℃	-	±0.7	-	mV/℃

Absolute Maximum Ratings

Description	Symbol	Range	Unit
Maximum input voltage	V_{IN_max}	15	V
Power dissipation	P_D	400	mW
Operating temperature	T_W	-25～+70	℃
Storage temperature range	T_C	-50～+125	℃
Welding temperature	T_H	260	℃，10s

Pin Diagram(top view)



Typical Application